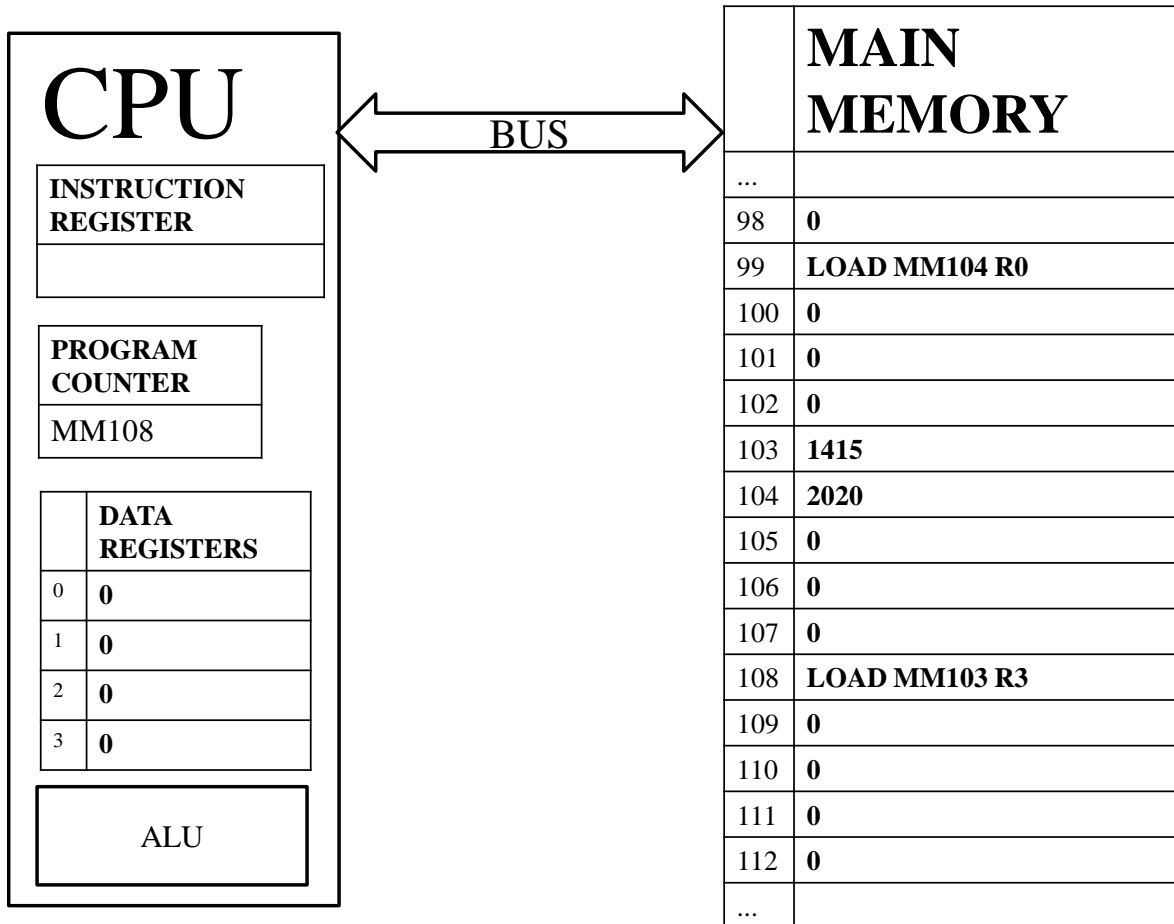Assume you start with the situation as shown here, and one instruction cycle has just ended. Explain in words what happens at each step of the next instruction cycle, starting with Fetch, for this **specific** situation. Diagrams are not required.

I am only asking about one instruction cycle. You do not need to add any more instructions. You do not need to add any more data. (Data may be modified by the program itself)

CPU	BUS	MAIN MEMORY	
		...	
INSTRUCTION REGISTER		98	**0**
		99	**LOAD MM104 R0**
		100	**0**
		101	**0**
PROGRAM COUNTER		102	**0**
MM108		103	**1415**
		104	**2020**
DATA REGISTERS		105	**0**
0 **0**		106	**0**
1 **0**		107	**0**
2 **0**		108	**LOAD MM103 R3**
3 **0**		109	**0**
		110	**0**
ALU		111	**0**
		112	**0**
		...	

Instruction Definitions:

LOAD MM104 R0
    Copy the data from Main Memory location 104 to Register #0

LOAD MM103 R3
    Copy the data from Main Memory location 103 to Register #3